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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/034,233	12/27/2001	Bharath Rangarajan	G0214	9100

23623 7590 10/20/2004

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EXAMINER

ALANKO, ANITA KAREN

ART UNIT	PAPER NUMBER
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1765

DATE MAILED: 10/20/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>		<b>Applicant(s)</b>	
	10/034,233		RANGARAJAN ET AL.	
	<b>Examiner</b>		<b>Art Unit</b>	
	Anita K Alanko		1765	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 7/30/04, 8/10/04 amdt&reply to interview.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 20-33 and 41-61 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 20-33 and 41-61 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |   |   |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)                        | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)    | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____   | 6) <input type="checkbox"/> Other: _____                                    |

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***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 23-33 and 41-61 are rejected under 35 U.S.C. 103(a) as being unpatentable over the combination of Moore (US 2001/0029152 A1) and Ishizaki et al (US 2001/0041509 A1).

Moore discloses a method for controlling a chemical mechanical polishing process comprises gathering electrical resistance information and analyzing to control the CMP process (paragraph [0055]-[0056] and also [0044-46, 48-49]) for a semiconductor wafer with integrated circuit features (see abstract and paragraph [0002]).

Moore fails to disclose that the electrical resistance members are embedded in the wafer. Ishizaki teaches that it is a useful technique to use embedded electrical resistance members to control a polishing process ([0004]-[0005]). It would have been obvious to one with ordinary skill in the art to use embedded electrical resistance members to control the polishing process of Moore because Ishizaki teaches that this is a useful technique.

As to amended claims 23, 41 and 52, Moore is directed to integrated circuits, therefore it is obvious that in the method of Moore modified by Ishizaki that the resistance members are embedded in the wafer with integrated circuit features since Moore is specifically directed to semiconductor processing. It is noted that Ishizaki is not merely for thin film magnetic heads, but for various kinds of workpieces ([0068]), for which integrated circuits are obvious.

As to claim 41, it would have been obvious to one with ordinary skill in the art to isolate a plurality of electrical resistance members within different layers and to couple them to form an individual electrical resistance entity across the different layers in the modified method of Moore in order to increase the sensitivity of the control by using more members.

As to claim 52, it would have been obvious to one with ordinary skill in the art to form at least two electrical resistance members in different directions within a layer and to electrically isolate them from one another in the modified method of Moore in order to increase the sensitivity of the control by using more members.

### ***Response to Arguments***

Applicant's arguments filed 7/30/04 and 8/10/04 are noted, and have been fully considered, but they are not persuasive. Applicant had noted that Ishizaki is directed to ceramic materials. However, polishing the ceramic alumina and controlling a polishing process, as in Ishizaki, is also useful in the formation of integrated circuits since thin films of alumina are present in both thin film magnetic heads and in integrated circuits. Further it is noted that the primary reference is directed to semiconductors, so the arguments are not persuasive since control of polishing of semiconductors is known and desired, as taught by Moore.

### ***Conclusion***

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anita K Alanko whose telephone number is 571-272-1458. The examiner can normally be reached on Mon-Fri until 2:30 pm (Wed until 11:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nadine Norton can be reached on 571-272-1465. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

*Anita K. Alanko*  
Anita K Alanko  
Primary Examiner  
Art Unit 1765